HARDWARE DESIGN **MASTERCLASSES**

Hardware Design Analog/Digital design PCB and SCH development Signal Processing Case Studies Space electronics

WROCŁAW, Poland 15 APRIL 2024

HARDWARE DESIGN MASTERCLASSES

15 April 2024

Park Hotel Diament - Muchoborska 10, Wrocław, Poland **Basic information about the conference**

Target Audience

The main goal of Hardware Design Masterclasses event is the exchange of knowledge and experience among professionals in hardware design, such as:

- 1. Hardware design engineers
- 2. Principal hardware engineers, technical lead engineers
- 3. R&D engineers
- 4. Managers of hardware design and R&D activities
- 5. Technical project managers
- 6. Field Application Engineers and any other technical supporting persons
- 7. Directors and managers of R&D centers

Admission to the conference is free of charge. There are 120 seats available for participants. Registration in advance is required to attend the event.

Conference profile

Hardware Design Masterclasses conference profile fills the gap on the Polish Conference market as it focuses on practical approaches in professional hardware design. Hardware Design Masterclasses creates a space for professionals to exchange knowledge, to acquire business relations and to develop future cooperation possibilities. This first edition was strongly focused on key electronics design technologies such as: High-speed & RF technologies, space electronics, analog, digital and mixed signal electronics as well as PCB/SCH development and circuit simulations. Conference topics were selected by questionnaires that were submitted by hardware engineers.

Second edition is focused on the electronics areas such as: analog and digital design, electromagnetic compatibility, power supply design, space electronics, design for manufacturing and more.

Hardware Design Masterclasses sessions are divided into case-study lectures including Q&A sessions. Hardware Design Masterclasses aims for practical approach, use-cases, live measurements or design, practical exercises and experiments. Wrocław's location, where the event takes place, is Poland's "Silicon Valley" with numerous IT and High-Tech companies headquartered here.

Apart from the meritorical content, the conference is also to be a platform for leading electronic companies to present their profile and products on exhibit stands. Detailed information is provided in section "Sponsorship offer".

Company details

_HARDWARE DESIGN MASTERCLASSES

Hardware Design
Analog/Digital design
PCB and SCH development
Signal Processing
Case Studies
Space electronics
EMC

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First edition (13 November 2023) - photographs from the event:













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HARDWARE DESIGN MASTERCLASSES WROCŁAW, Poland 15 APRIL 2024 Hardware Design Analog/Digital design PCB and SCH development Signal Processing Case Studies Space electronics EMC

Venue and date

Conference is taking place in Park Hotel Diament, Muchoborska 10, Wrocław, Poland, 15th of April 2024. The total number of participants is limited to 120 attendants

Participants registration status for today (22/02/2024) - all seats are booked.

Sponsorship offer

Hardware Design Masterclasses gathers professionals in the field of electronics and thus will provide a unique environment for building cooperation, acquiring new professional contacts as well as developing future business opportunities related in the fields of hardware components, measurement equipment and services. Our offer of sponsorship package includes the following benefits:

- 1. attendance in the full conference program for two company's representatives
- 2. company's exhibition stand
- 3. conference materials
- 4. optional: possibility to present your company by conducting workshop or lecture
- 5. any additional sponsorship requests may to be individually discussed (kontakt@doktortronik.pl)

Conference organizer

The conference will be organized by Rafał Stępień that has its own brand DoktorTronik (www.doktortronik.pl). I specialize in providing technical trainings in the fields of high speed and RF designs as well as DC/DC converters, signal processing, IoT and circuit theory. Owner of the company, Rafał Stępień, Ph.D., has more than 20 years of experience in designing electronics circuits. Currently he holds a position as a Technical Director at Bustec Ltd. and Technical Advisor for the Board of Management of BCAST Ltd. in the fields of IoT and DAB+ technology.

